

At page 10, line 24, please replace "masking" with --mask--.

At page 11, line 6, please replace "hard mask 135" with  
--mask layer 135--.

At page 11, line 9, please replace "masking" with --mask--.

At page 11, line 17, please delete "region".

At page 11, lines 2 and 21, please replace "masking" with  
--mask--.

At page 12, line 8, please replace "masking" with --mask--.

At page 13, line 26, please replace "P-well" with --P-type  
well--.

At page 14, line 1, please replace "P-well" with --P-type  
well--.

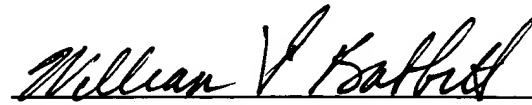
At page 17, line 8, please replace "of a CMOS circuit to  
optimum" with --to improve NMOS and PMOS--.

At page 17, line 13, please delete "layer".

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN, LLP

Dated: 3/2/00



William Thomas Babbitt, Reg. No. 39,591

12400 Wilshire Boulevard  
Seventh Floor  
Los Angeles, California 90025  
(310) 207-3800